EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"10/024720"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L2	12	("4459699"|"5995819"|"6148047"|"6356218"|"6366622"|"6678178").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L3	3390	phase with shift$2 with (LPF or (low adj pass adj filter))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L4	2980	(phase adj shift$2) with (LPF or (low adj pass adj filter))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L5	944289	(DC or (direct adj current) near offset) (phase adj shift$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L6	3174	(DC or (direct adj current) near offset) with (phase adj shift$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L7	165	(DC or (direct adj current) near offset) with adjust$3 with(phase adj shift$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L8	165	(DC or (direct adj current) near offset) with adjust$3 with (phase adj shift$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39

EAST Search History

L9	267	(DC or (direct adj current) near offset) with (adjust$3 or correct$3) with (phase adj shift$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L10	3	(DC or (direct adj current) near offset) with (adjust$3 or correct$3) with (phase adj shift$2) with estimat$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L11	6	(4385328|4459699|6108696).pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L12	12	(DC or (direct adj current) near offset) with (adjust$3 or correct$3) with (phase adj shift$2) with compar$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L13	18	(DC or (direct adj current) near offset) with GFSK	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L14	3	(DC or (direct adj current) near offset) with GFSK and (phase with shift$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L15	38	(DC or (direct adj current) near offset) with (adjust$3 or correct$3) with ((phase adj shift$2) or delay$3) with compar$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L16	3	(DC or (direct adj current) near offset) with GFSK and (delay$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39

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L17	53	Bluetooth and ((DC adj offset) with estimat$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L18	3296	375/346	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L19	1178	(DC or (direct adj current) near offset) with (adjust$3 or correct$3) with compar$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L20	6	L18 and L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L21	164	((DC or (direct adj current) near offset) and (adjust$3 or correct$3) and compar$3 and (phase with shift$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L22	0	((DC or (direct adj current) near offset) and (adjust$3 or correct$3) and compar$3 and (phase with shift$3) and (estimat$3 adj cd adj offset)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L23	0	((DC or (direct adj current) near offset) and (adjust$3 or correct$3) and compar$3 and (phase with shift$3) and (estimat$3 with cd adj offset)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39
L24	10	((DC or (direct adj current) near offset) and (adjust$3 or correct$3) and compar$3 and (phase with shift$3) and (estimat$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39

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L25	10	((DC or (direct adj current) adj offset) and (adjust$3 or correct$3) and compar$3 and (phase with shift$3) and (estimat$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/16 15:39

Web Results **1 - 10** of about **165** for **"DC offset" adjusting and comparing "phase shifting" estimated**. (**0.66** second

[PDF] The Phase Sensitive (Lock-in) Detector
File Format: PDF/Adobe Acrobat - View as HTML
To compensate for a small **DC offset**, make your measurements at both 0 ... In the center (HOLD) position, the **phase shifting** is stopped ...
courses.washington.edu/phys431/lockin.pdf

[PDF] DCATT Kona paper final.doc
File Format: PDF/Adobe Acrobat - View as HTML
of measuring and **comparing** the final achieved wavefront. ... The baseline design for the IWFS is a modified **phase-shifting** Mach-Zehnder interferometer, ...
www.jwst.nasa.gov/public/doc_0157/rev_02/3356-72.pdf

[PDF] PubTeX output 1997.11.17:1150
File Format: PDF/Adobe Acrobat - View as HTML
A **comparison** of total capacitance. used by the filters and ADC is given in Table III. It was. **estimated** that by scaling the capacitance, a reduction of 40% ...
kabuki.eecs.berkeley.edu/~gchien/ papers/jssccDec97/DECTreceiver.pdf

Integrated CMOS Transceivers for Wireless Communications
One important error source is the device mismatch induced **DC offset** and 1/f noise of ...
LOs with an on-chip **phase shifting** network adjusted for quadrature. ...
www.stanford.edu/~sols/course_projects/EE359proj.shtml

[PDF] PN9000 Phase Noise
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and **phase shifting** one path "Ps" by 90°. The two signal paths RF and LO enter the mixer ... This way the **DC offset** and the internal phase noise of the phase ...
www.aeroflex.com/products/gentest/ phasenoise/productinfo/PN9000Productline.pdf

From EDN Europe: Direct conversion receivers battle superhets for ...
To provide accurate **phase shifting** and avoid in-phase/quadrature (I/Q) mismatches, ...
GPRS and EDGE data exchanges require especially accurate **dc offset** ...
www.edn.com/article/CA84071.html

[PDF] Interference Potential of Ultrawideband Signals
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In this study, we investigated how DS-UWB statistics **compare** with Gaussian ... **DC offset** settings are found by repetitive **adjustment** of these settings and ...
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The filter-frequency response requires no user **adjustment**. To achieve low LO leakage at the RF output in a ZIF. system, the **DC offset** of the Tx baseband ...
pdfserv.maxim-ic.com/en/ej/EJ50.pdf

[PDF] White Light Interferometry Using Pancharatnam's Phase
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Thus phase shifts of π/2 can be implemented by **adjusting** d in ... In order to carry out **phase shifting** with white light, Pancharatnam's phase is ap- ...
www.phy.auckland.ac.nz/thesis/Sam_LO_MSc.pdf

[PDF] Broad-Band (20 GHz) Laser-Diode-Based Optoelectronic Microwave ...
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As a result, the maximum **phase-shifting** range is reduced. Thus, an optimal design of the phase-**comparing** circuit for. achieving larger phase sensitivity ...
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Web Results **1** - **1** of **1** for **"estimated DC offset" adjusting and comparing "phase shifting" estimated**. (**0.37** secon

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the **estimated DC offset** voltage. Since the load of the OpAmp is resistive, ... both modulation formats are achieved by **phase shifting** as expressed in (5.1). ...
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Lagimoniere, Jr., Ernest Eugene, Aug 2001
This last decade experienced a sudden increase of interest in the control of thermo-acoustic instabilities, in particular through the use of fuel modulation techniques. The primary goal of this research was to design, construct and characterize a high ...
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5. A 1-bit superconductive phase shifter in coplanar waveguide at Ka-band

☐ **Mycroft, Walter**, Jan 1999
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BOWLER, Colin, E., J. / LARSEN, Einar, V. / BAKER, Daniel, H. / ELECTRIC POWER RESEARCH INSTITUTE, *EUROPEAN PATENT*, May 1994
...subsynchronous oscillations and direct current (**DC**) **offset**. Left unchecked, subsynchronous resonance...damping subsynchronous oscillations and **DC offset** in power transmission systems. For example...transmission system, as well as to damp **DC offset**, which is directed toward overcoming...
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BOWLER, Colin, E., J. / LARSEN, Einar, V. / BAKER, Daniel, H. / ELECTRIC POWER RESEARCH INSTITUTE, *PATENT COOPERATION TREATY APPLICATION*, Nov 1993
...subsynchronous oscillations and direct current (**DC**) **offset**. Left unchecked, subsynchronous resonance...damping subsynchronous oscillations and **DC offset** in power transmission systems. For example...transmission system, as well as to damp **DC offset**, which is directed toward overcoming...
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1. **Design and Analysis of CMOS Broad-Band Compact High-Linearity Modulators for Gigabit Microwave/Millimeter-Wave Applications**
Chang, H.-Y.; Wu, P.-S.; Huang, T.-W.; Wang, H.; Chang, C.-L.; Chern, J.G.J.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 54, Issue 1, Jan. 2006 Page(s):20 - 30
Digital Object Identifier 10.1109/TMTT.2005.860900
AbstractPlus | Full Text: PDF(1168 KB) IEEE JNL
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2. **A low-cost dual-mode noncoherent receiver with robust frequency-offset compens**
Liu, C.-L.; Djen, W.S.; Feher, K.;
Vehicular Technology Conference, 1993 IEEE 43rd
18-20 May 1993 Page(s):412 - 415
Digital Object Identifier 10.1109/VETEC.1993.507499
AbstractPlus | Full Text: PDF(332 KB) IEEE CNF
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3. **An I/Q active balanced harmonic mixer with IM2 cancelers and a 45° phase shifter**
Yamaji, T.; Tanimoto, H.; Kokatsu, H.;
Solid-State Circuits, IEEE Journal of
Volume 33, Issue 12, Dec. 1998 Page(s):2240 - 2246
Digital Object Identifier 10.1109/4.735708
AbstractPlus | References | Full Text: PDF(520 KB) IEEE JNL
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4. **Zero-crossing Shapiro step in a three-junction SQUID magnetically coupled with t phase-shifted RF signals**
Mizugaki, Y.; Jian Chen; Nishikata, S.; Sugi, K.; Nakajima, K.; Yamashita, T.;
Applied Superconductivity, IEEE Transactions on
Volume 13, Issue 2, Part 1, June 2003 Page(s):926 - 929
Digital Object Identifier 10.1109/TASC.2003.814084
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5. **Phased array control using phase-locked-loop phase shifters**
Houghton, A.W.; Brennan, P.V.;
Microwaves, Antennas and Propagation, IEE Proceedings H
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AbstractPlus | Full Text: PDF(404 KB) IEE JNL

6. **Complete elimination of DC offset in current signals for relaying applications**
Jun-Zhe Yang; Chih-Wen Liu;
Power Engineering Society Winter Meeting, 2000. IEEE
Volume 3, 23-27 Jan. 2000 Page(s):1933 - 1938 vol.3
Digital Object Identifier 10.1109/PESW.2000.847649
AbstractPlus | Full Text: PDF(312 KB) IEEE CNF
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7. **A new family of measurement technique for tracking voltage phasor, local system frequency, harmonics and DC offset**
Jun-Zhe Yang; Chih-Wen Liu;
Power Engineering Society Summer Meeting, 2000. IEEE
Volume 3, 16-20 July 2000 Page(s):1327 - 1332 vol. 3
Digital Object Identifier 10.1109/PESS.2000.868716
AbstractPlus | Full Text: PDF(424 KB) IEEE CNF
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8. **A new phase-feedback digital oscillator for HHFW transmitters**
Greenough, N.; Lafrance, D.;
Fusion Engineering, 2003. 20th IEEE/NPSS Symposium on
14-17 Oct. 2003 Page(s):504 - 507
Digital Object Identifier 10.1109/FUSION.2003.1426694
AbstractPlus | Full Text: PDF(1721 KB) IEEE CNF
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9. **A low-noise direct conversion PSK receiver for TDMA land mobile communication**
Hayashi, R.; Nakajima, T.; Shimozawa, M.; Miyake, M.; Fujino, T.;
Personal, Indoor and Mobile Radio Communications, 1997. 'Waves of the Year 2000'. PI '97., The 8th IEEE International Symposium on
Volume 3, 1-4 Sept. 1997 Page(s):854 - 857 vol.3
Digital Object Identifier 10.1109/PIMRC.1997.627007
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10. **Differential detection of π/4-shifted-DQPSK for digital cellular radio**
Chennakeshu, S.; Saulnier, G.J.;
Vehicular Technology Conference, 1991. 'Gateway to the Future Technology in Motion', IEEE
19-22 May 1991 Page(s):186 - 191
Digital Object Identifier 10.1109/VETEC.1991.140474
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11. **Applications of Lyapunov criteria in phase measurements using conformal mappi**
Gundrum, H.C.; Rizkalla, M.E.;
Circuits and Systems, 1994., Proceedings of the 37th Midwest Symposium on
Volume 2, 3-5 Aug. 1994 Page(s):1295 - 1298 vol.2
Digital Object Identifier 10.1109/MWSCAS.1994.519046
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12. **A low-power multirate differential PSK receiver for space applications**
Yuce, M.R.; Wentai Liu;
Vehicular Technology, IEEE Transactions on
Volume 54, Issue 6, Nov. 2005 Page(s):2074 - 2084
Digital Object Identifier 10.1109/TVT.2005.858196
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13. **A direct-conversion receiver for 900 MHz (ISM band) spread-spectrum digital cord telephone**
Hull, C.D.; Joo Leong Tham; Chu, R.R.;

Solid-State Circuits, IEEE Journal of
Volume 31, Issue 12, Dec. 1996 Page(s):1955 - 1963
Digital Object Identifier 10.1109/4.545818

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14. **Subharmonically pumped CMOS frequency conversion (up and down) circuits for WCDMA direct-conversion transceiver**
Kwang-Jin Koh; Mun-Yang Park; Cheon-Soo Kim; Hyun-Kyu Yu;
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Volume 39, Issue 6, June 2004 Page(s):871 - 884
Digital Object Identifier 10.1109/JSSC.2004.827792

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15. **Noncoherent detection of digitally phasor block-modulated signals in the presenc offset**
Char-Dir Chung;
Vehicular Technology Conference, 2004. VTC2004-Fall. 2004 IEEE 60th
Volume 3, 26-29 Sept. 2004 Page(s):2118 - 2122 Vol. 3
Digital Object Identifier 10.1109/VETECF.2004.1400414

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16. **An adaptive direct conversion transmitter**
Hilborn, D.S.; Stapleton, S.P.; Cavers, J.K.;
Vehicular Technology, IEEE Transactions on
Volume 43, Issue 2, May 1994 Page(s):223 - 233
Digital Object Identifier 10.1109/25.293640

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17. **BER performance analysis of a direct conversion receiver**
Lindoff, B.; Malm, P.;
Communications, IEEE Transactions on
Volume 50, Issue 5, May 2002 Page(s):856 - 865
Digital Object Identifier 10.1109/TCOMM.2002.1006566

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18. **A GFSK demodulator for low-IF Bluetooth receiver**
Bo Xia; Chunyu Xin; Wenjun Sheng; Valero-Lopez, A.Y.; Sanchez-Sinencio, E.;
Solid-State Circuits, IEEE Journal of
Volume 38, Issue 8, Aug. 2003 Page(s):1397 - 1400
Digital Object Identifier 10.1109/JSSC.2003.814424

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19. **Efficient compensation for frequency-dependent errors in analog reconstruction fi used in IQ modulators**
Tuthill, J.; Cantoni, A.;
Communications, IEEE Transactions on
Volume 53, Issue 3, March 2005 Page(s):489 - 496
Digital Object Identifier 10.1109/TCOMM.2005.843455

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20. **A new method of an IF I/Q demodulator for narrowband signals**
Xue, R.; Xu, Q.; Chang, K.F.; Tam, K.W.;
Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on
23-26 May 2005 Page(s):3817 - 3820 Vol. 4
Digital Object Identifier 10.1109/ISCAS.2005.1465462

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21. **Good and bad training sequences for zero IF sampling edge receivers**
Krueger, M.; Denk, R.; Yang, B.;
Acoustics, Speech, and Signal Processing, 2004. Proceedings. (ICASSP '04). IEEE International Conference on
Volume 4, 17-21 May 2004 Page(s):iv - 1033-6 vol.4
Digital Object Identifier 10.1109/ICASSP.2004.1327006
AbstractPlus | Full Text: PDF(260 KB) IEEE CNF
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22. **Improving QPSK demodulator performance for quadrature receiver with informati amplitude and phase imbalance correction**
Hung Nguyen;
Wireless Communications and Networking Conference, 2000. WCNC. 2000 IEEE
Volume 3, 23-28 Sept. 2000 Page(s):1440 - 1444 vol.3
Digital Object Identifier 10.1109/WCNC.2000.904844
AbstractPlus | Full Text: PDF(332 KB) IEEE CNF
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23. **Using a direct conversion receiver in EDGE terminals-a new DC offset compensati algorithm**
Lindoff, B.;
Personal, Indoor and Mobile Radio Communications, 2000. PIMRC 2000. The 11th IEEE International Symposium on
Volume 2, 18-21 Sept. 2000 Page(s):959 - 963 vol.2
Digital Object Identifier 10.1109/PIMRC.2000.881564
AbstractPlus | Full Text: PDF(324 KB) IEEE CNF
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24. **Effect of DC offset on performance of differentially detected π/4 DQPSK**
Huang, S.; Stonick, J.T.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE Interna Symposium on
Volume 4, 28-31 May 2000 Page(s):757 - 760 vol.4
Digital Object Identifier 10.1109/ISCAS.2000.858862
AbstractPlus | Full Text: PDF(288 KB) IEEE CNF
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25. **40 Mbit/s adaptive MLSE equalizer LSI and its performance in 5 GHz-band transmi system**
Shirato, Y.; Kobayashi, K.; Denno, S.;
Vehicular Technology Conference, 1999 IEEE 49th
Volume 1, 16-20 May 1999 Page(s):305 - 308 vol.1
Digital Object Identifier 10.1109/VETEC.1999.778066
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   Chang, H.-Y.; Wu, P.-S.; Huang, T.-W.; Wang, H.; Chang, C.-L.; Chern, J.G.J.;
   Microwave Theory and Techniques, IEEE Transactions on
   Volume 54, Issue 1, Jan. 2006 Page(s):20 - 30
   Digital Object Identifier 10.1109/TMTT.2005.860900
   AbstractPlus | Full Text: PDF(1168 KB) IEEE JNL
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2. **A new family of measurement technique for tracking voltage phasor, local system frequency, harmonics and DC offset**
   Jun-Zhe Yang; Chih-Wen Liu;
   Power Engineering Society Summer Meeting, 2000. IEEE
   Volume 3, 16-20 July 2000 Page(s):1327 - 1332 vol. 3
   Digital Object Identifier 10.1109/PESS.2000.868716
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IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

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27. An offset-free LPF for π/4-shift QPSK signal generator
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Inventor Information for 10/024720

Inventor Name	City	State/Country
SCHETELIG, MARKUS	WALTROP	GERMANY
BURGESS, PAUL	BOCHUM	GERMANY

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Last Name = SCHETELIG
First Name = MARKUS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08851369	6044118	150	05/05/1997	METHOD FOR ADJUSTING THE FREQUENCY OF AN OSCILLATOR FOR A RECEIVER CIRCUIT	SCHETELIG, MARKUS
09267030	6487400	150	03/12/1999	COMMUNICATIONS DEVICE AND A METHOD FOR CONTROL OF ITS OPERATION	SCHETELIG, MARKUS
09404654	6959013	150	09/24/1999	COMMUNICATION NETWORK	SCHETELIG, MARKUS
09404657	6639905	150	09/24/1999	COMMUNICATION NETWORK	SCHETELIG, MARKUS
09404717	6532228	150	09/24/1999	OPEN LOOP RECEIVER	SCHETELIG, MARKUS
09404718	6622011	150	09/24/1999	PAGING	SCHETELIG, MARKUS
09832113	6895229	150	04/10/2001	RECEIVER ARRANGEMENT FOR RECEIVING FREQUENCY-MODULATED RADIO SIGNALS AND METHODS OF ADAPTING AND TESTING A RECEIVING BRANCH OF THE RECEIVER ARRANGEMENT	SCHETELIG, MARKUS
09885130	Not Issued	121	06/21/2001	DEVICE SYNCHRONISATION OVER A NETWORK	SCHETELIG, MARKUS
09889232	Not Issued	83	11/28/2001	Interface	SCHETELIG, MARKUS
09981795	Not Issued	71	10/19/2001	Method and a device for controlling data extraction from a data stream containing at lease one data packet	SCHETELIG, MARKUS
09981903	Not Issued	93	10/19/2001	METHOD AND DEVICE FOR IDENTIFYING A DATA PACKET IN A DATA STREAM	SCHETELIG, MARKUS
10024720	Not Issued	51	12/21/2001	Signal DC offset correction method and device	SCHETELIG, MARKUS
60243061	Not Issued	159	10/25/2000	Bluetooth2 high rate radio	SCHETELIG, MARKUS

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Last Name = BURGESS
First Name = PAUL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08178407	5427159	250	01/06/1994	COUNTERTOP FINISHING APPARATUS	BURGESS, II, PAUL B.
09142876	Not Issued	161	09/16/1998	IMPROVEMENTS RELATING TO TAPERED FISHING LINES	BURGESS, PAUL
09397854	6459885	150	09/17/1999	RADIO TRANSCEIVER SWITCHING CIRCUIT	BURGESS, PAUL
09404717	6532228	150	09/24/1999	OPEN LOOP RECEIVER	BURGESS, PAUL
09832113	6895229	150	04/10/2001	RECEIVER ARRANGEMENT FOR RECEIVING FREQUENCY-MODULATED RADIO SIGNALS AND METHODS OF ADAPTING AND TESTING A RECEIVING BRANCH OF THE RECEIVER ARRANGEMENT	BURGESS, PAUL
09889232	Not Issued	83	11/28/2001	Interface	BURGESS, PAUL
10024720	Not Issued	51	12/21/2001	Signal DC offset correction method and device	BURGESS, PAUL
10111600	6693593	150	09/12/2002	HIGH FREQUENCY CIRCUIT WITH A CONNECTION FOR A PRINTED ANTENNA	BURGESS, PAUL
10433745	Not Issued	95	11/04/2003	RADIO TRANSCEIVER HAVING A PHASE-LOCKED LOOP CIRCUIT	BURGESS, PAUL
10451163	Not Issued	41	11/26/2003	Radio transmission device and method for aligning parameters thereof	BURGESS, PAUL
11405875	Not Issued	20	04/17/2006	Radio transceiver having a phase-locked loop circuit	BURGESS, PAUL
60532692	Not Issued	159	12/29/2003	Highway helper - a.k.a. - the spotter	BURGESS, PAUL
07298885	4952344	150	03/13/1989	METHOD OF AND APPARATUS FOR MAKING A FISHING LINE	BURGESS, PAUL D.
07679741	5109836	150	04/03/1991	BREATHING APPARATUS FOR PROVIDING A SOURCE OF BREATHABLE AIR IN A BURNING STRUCTURE	BURGESS, PAUL D.

07769186	5163422	150	09/30/1991	BREATHING APPARATUS FOR PROVIDING A SOURCE OF BREATHABLE AIR IN A BURNING STRUCTURE	BURGESS, PAUL D.
07067284	4799417	150	06/29/1987	BRAIDED LINE	BURGESS, PAUL DAVID
06628566	4541911	150	07/06/1984	METHOD OF ASSEMBLING A FILTER PRESS TYPE ELECTROLYTIC CELL	BURGESS, PAUL E. A.
07112891	4824542	150	10/27/1987	FILTER PRESS STRUCTURE OR CELL AND METHOD OF ASSEMBLING SAME	BURGESS, PAUL E. A.
07148610	4879597	250	01/26/1988	PROCESSING OF VIDEO IMAGE SIGNALS	BURGESS, PAUL K.
09597430	Not Issued	61	06/20/2000	Packetizing telecommunications switch	BURGESS, PAUL NORMAN
09366896	7027575	150	08/04/1999	METHOD AND APPARATUS FOR ASSIGNING TELEPHONE NUMBERS	BURGESS, PAUL NORMAN

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